

ABSTRACT OF THE DISCLOSURE

A packet buffer management method and system are provided which enable maximum utilization of the hardware resources of the buffer memory, and which are optimum for
5 the number of preset physical ports in use or for the number of service class (CoS) areas. In the buffer memory management method in a packet transmission/reception device, for storing a received packet in the buffer memory and controlling writing and reading of packets to and from the
10 above buffer memory, the control methods are set in units of the service class contained in the header portion of received packets, and areas allocated to each service class in the buffer memory storing received packets are modified according to the set number of the above service classes.

15